Related Work Outline

1. Motivation: Data Integrity Threat Model for Processor-Memory Architecture
   1. Threat model for processor-memory system
2. Integrity Protection: Theoretical Models
   1. Cryptographic hash function
   2. MAC scheme
      1. Deterministic MAC
      2. Stately MAC
   3. Block-level AREA
3. Security Evaluation for Authentication Primitives
   1. Computational Provable Security
      1. Security Notion
      2. Security and Randomness
   2. Formal method model
   3. Automated sound security analysis
4. Design Works of Authentication Primitives
   1. Cryptographic hash function
   2. MAC scheme
   3. Block-level AREA
5. Message Authentication (MA) System: Implementation of Theoretical Models
   1. Based on Cryptographic hash function
      1. Uniprocessor
      2. Multiprocessor
   2. Based on MAC scheme
      1. Uniprocessor
      2. Multiprocessor
   3. Based on Block-level AREA
      1. Uniprocessor
      2. Multiprocessor